



M. C. E. Society's
Abeda Inamdar Senior College
Of Arts, Science and Commerce, Camp, Pune-1
(Autonomous) Affiliated to Savitribai Phule Pune
University NAAC accredited 'A' Grade

B.C.A. (Science) (Minor) as per NEP

(CBCS – Autonomy 21 Pattern)

Course Offered as	Minor (Practical)
Course/ Paper Title	Lab I-Computer Organization
Course Code	23SBCA32MN
Semester	III
No. of Credits	2
No of Hours	30

Note: - Each Practical batch will have maximum 15 students.

Sr. No	Title of Experiment
1	Study of Basic Logic Gates (Verification of Truth tables)
2	Study of Derived Logic Gates (Verification of Truth tables)
3	Study Of De Morgan's Theorem
4	Study of Binary to Gray & Gray to Binary Converter (K- Map based design).
5	Study of Half Adder and Full Adder using Logic Gates.
6	Study of Half Subtractor using Logic Gates.
7	Study of Decimal to BCD (Binary) Converter using Gates.
8	Study of Multiplexer and Demultiplexer
9	Study of flip flops.

10	Study of counter ICs: IC 7490 and designing Mod-N counters
11	Study of Asynchronous Up/Down Counter
12	Study of Synchronous Up/Down Counter
13	Study of Shift Registers
14	Study of Four-bit ALU