



**M. C. E. Society's**  
**Abeda Inamdar Senior College**  
 Of Arts, Science and Commerce, Camp, Pune-1  
 (Autonomous) Affiliated to Savitribai Phule Pune  
 University NAAC accredited 'A' Grade

## **B.C.A. (Science) (Minor) as per NEP**

**(CBCS – Autonomy 21 Pattern)**

<b>Course Offered as</b>	Minor (Practical)
<b>Course/ Paper Title</b>	Lab I-Computer Organization
<b>Course Code</b>	<b>23SBCA32MN</b>
<b>Semester</b>	III
<b>No. of Credits</b>	2
<b>No of Hours</b>	30

Note: - Each Practical batch will have maximum 15 students.

<b>Sr. No</b>	<b>Title of Experiment</b>
<b>1</b>	Study of Basic Logic Gates (Verification of Truth tables)
<b>2</b>	Study of Derived Logic Gates (Verification of Truth tables)
<b>3</b>	Study Of De Morgan's Theorem
<b>4</b>	Study of Binary to Gray & Gray to Binary Converter (K- Map based design).
<b>5</b>	Study of Half Adder and Full Adder using Logic Gates.
<b>6</b>	Study of Half Subtractor using Logic Gates.
<b>7</b>	Study of Decimal to BCD (Binary) Converter using Gates.
<b>8</b>	Study of Multiplexer and Demultiplexer
<b>9</b>	Study of flip flops.

<b>10</b>	Study of counter ICs: IC 7490 and designing Mod-N counters
<b>11</b>	Study of Asynchronous Up/Down Counter
<b>12</b>	Study of Synchronous Up/Down Counter
<b>13</b>	Study of Shift Registers
<b>14</b>	Study of Four-bit ALU